

## Simberian News: January 16, 2019

«GreetingLine»

If you are planning to attend [DesignCon 2019](#), we will be happy to see you at Simberian booth #503. Please stop by to see demo of Simbeor THz accelerated with distributed computing on Windows and Linux. We will demonstrate how to use Simbeor THz for material model identification, pre-layout and post-layout electromagnetic analyses. Automatic decompositional analysis in the upcoming version of Simbeor Board Analyzer will be demonstrated – almost all structures on test boards can now be simulated without manual adjustments of selectors and solvers! We will also demonstrate Simbeor SDK for design automation in C, C++ or matlab. Simbeor SDK enabled use of Simbeor technology in other EDA tools such as stackup planner in Altium Designer 19.

**Simberian's technology** will be featured in three DesignCon presentations. Dr. Yuriy Shlepnev will be talking about the **challenges in design of predictable interconnects for 112 Gbps links in and out of package** at the following panel

H. Wu, M. Shimanouchi, M. Resso, R. Mellitz, Y. Shlepnev, A. Ran, [Panel – 112-Gbps Package Challenges](#), Ballroom G, 4:45 - 6:00 PM, January 29th.

Also Yuriy will be co-presenting two papers with **new results on the material model identification methodology** with co-authors from Intel and HP:

A. Manukovsky, Y. Shlepnev, [Effect of PCB Fabrication Variations on Interconnect Loss, Delay, Impedance & Identified Material Models for 56-Gbps Interconnect Designs](#), Ballroom G, 10:00 - 10:45 AM, January 30th, 2019.

Y. Damgaci, Y. Choi, C. Cheng, Y. Shlepnev, [Simplify & Validate: A Single Structure Approach for Separate PCB Dielectric & Conductor Roughness Loss Characterization](#), Ballroom B, 10:00 - 10:45 AM, January 31st, 2019.

For more information on product pricing and availability or demo, contact Simberian at [info@simberian.com](mailto:info@simberian.com).

**Hope to see you at DesignCon,  
Team Simberian**

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